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FDT458P

30V P-Channel PowerTrench MOSFET

General Description

This P-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers, and battery chargers.

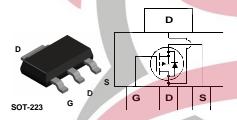
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable R_{DS(ON)} specifications.

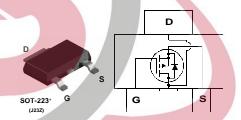
Applications

- · Battery chargers
- Motor drives

Features

- 3.4 A, -30 V. $R_{DS(ON)} = 130 \text{ m}\Omega$ @ $V_{GS} = 10 \text{ V}$ $R_{DS(ON)} = 200 \text{ m}\Omega$ @ $V_{GS} = 4.5 \text{ V}$
- · Fast switching speed
- Low gate charge (2.5 nC typical)
- High performance trench technology for extremely low R_{DS(ON)}
- High power and current handling capability in a widely used surface mount package





Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{DSS}	Drain-Source Voltage	- 30	V
V _{GSS}	Gate-Source Voltage	±20	V
l _D	Drain Current - Continuous (Note 1a)	3.4	Α
	- Pulsed	10	
P _D	Maximum Power Dissipation (Note 1a)	3.0	W
	(Note 1b)	1.3	
	(Note 1c)	ECTHONI	C
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics

R _{0JA}	Thermal Resistance, Junction-to-Ambient	(Note 1a)	42	°C/W
R _{0JC}	Thermal Resistance, Junction-to-Case	(Note 1)	12	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
458P	FDT458P	13"	12mm	2500 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics			I	I	
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30			V
ΔBV _{DSS} ΔT _J	Breakdown Voltage Temperature Coefficient	$I_D = -250 \mu\text{A}$, Referenced to 25°C		-23		mV/°C
loss	Zero Gate Voltage Drain Current	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μΑ
GSSF	Gate-Body Leakage, Forward	$V_{GS} = -25 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
GSSR	Gate-Body Leakage, Reverse	$V_{GS} = -25 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)				1	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	-1	-1.8	-3	V
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	$I_D = -250 \mu\text{A}$, Referenced to 25°C		4		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	$V_{GS} = -10 \text{ V}, I_D = -3.4 \text{ A}$ $V_{GS} = -4.5 \text{ V}, I_D = -2.7 \text{ A}$ $V_{GS} = -10 \text{ V}, I_D = -3.4 \text{ A}, T_J = 125^{\circ}\text{C}$		105 157 147	130 200 210	mΩ
I _{D(on)}	On-State Drain Current	$V_{GS} = -10 \text{ V}, V_{DS} = -5 \text{ V}$	-5			Α
g _{FS}	Forward Transconductance	$V_{DS} = -5 \text{ V}, \qquad I_{D} = -3.4 \text{ A}$		3		S
Dynamic	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V},$		205		pF
Coss	Output Capacitance	f = 1.0 MHz		55		pF
C _{rss}	Reverse Transfer Capacitance			26		pF
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -15 \text{ V}, I_D = -1 \text{ A},$		4.5	9	ns
r	Turn-On Rise Time	$V_{GS} = -10 \text{ V}, \qquad R_{GEN} = 6 \Omega$	7	12.5	23	ns
d(off)	Turn-Off Delay Time		7	11	20	ns
t _f	Turn-Off Fall Time			2	4	ns
Qg	Total Gate Charge	$V_{DS} = -15 \text{ V}, I_{D} = -3.4 \text{ A},$		2.5	3.5	nC
Q _{gs}	Gate-Source Charge	V _{GS} = -10 V		0.7		nC
Q _{gd}	Gate-Drain Charge			1		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings		•	•	
ls	Maximum Continuous Drain-Source				-2.5	Α
-	Drain-Source Diode Forward	$V_{GS} = 0 \text{ V}, I_S = -2.5 \text{ A} \text{(Note 2)}$		-0.8	-1.2	V

^{1.} R_{8UA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.



a) 42°C/W when mounted on a 1in2 pad of 2 oz copper



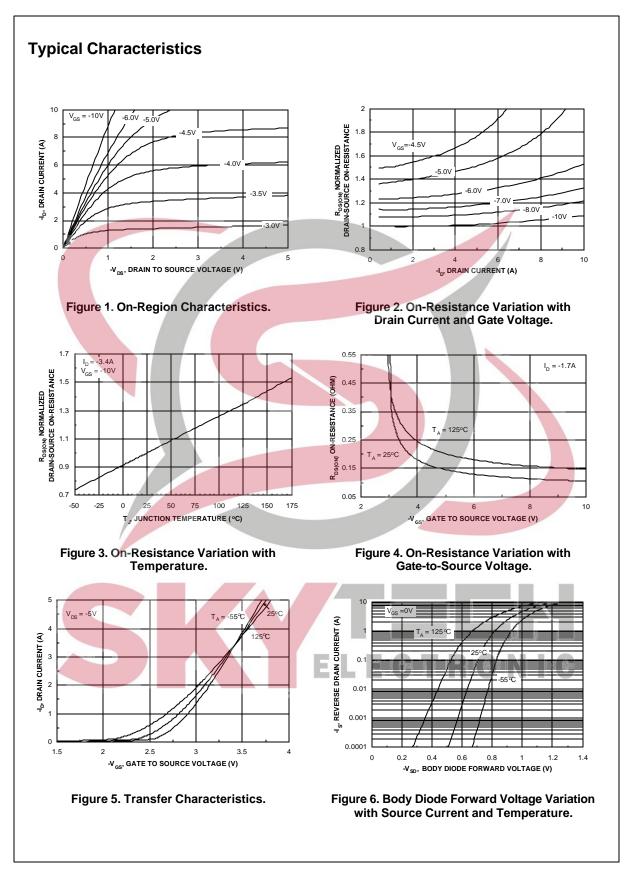
b) 95°C/W when mounted on a .0066 in² pad of 2 oz



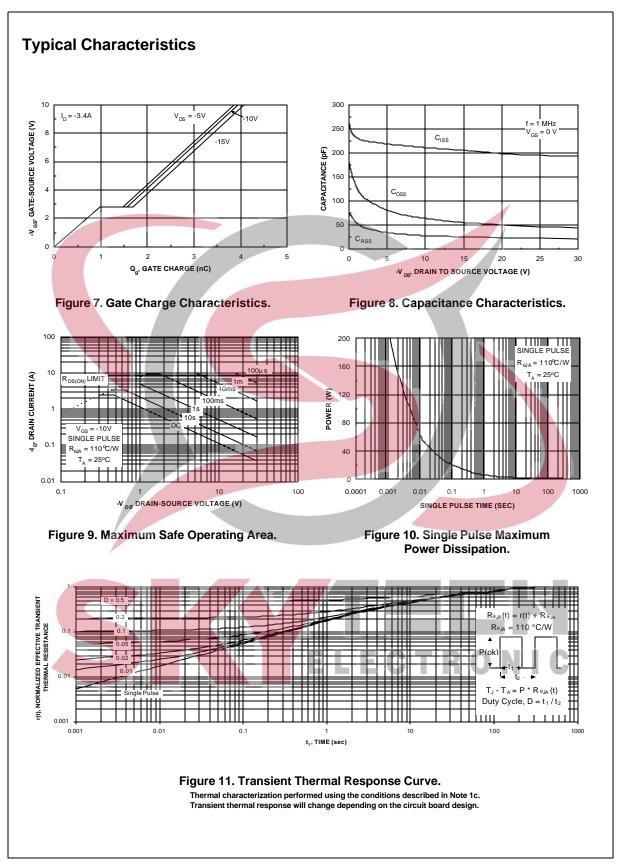
c) 110°C/W when mounted on a minimum pad.

2. Pulse Test: Pulse Width $< 300 \mu s$, Duty Cycle < 2.0%

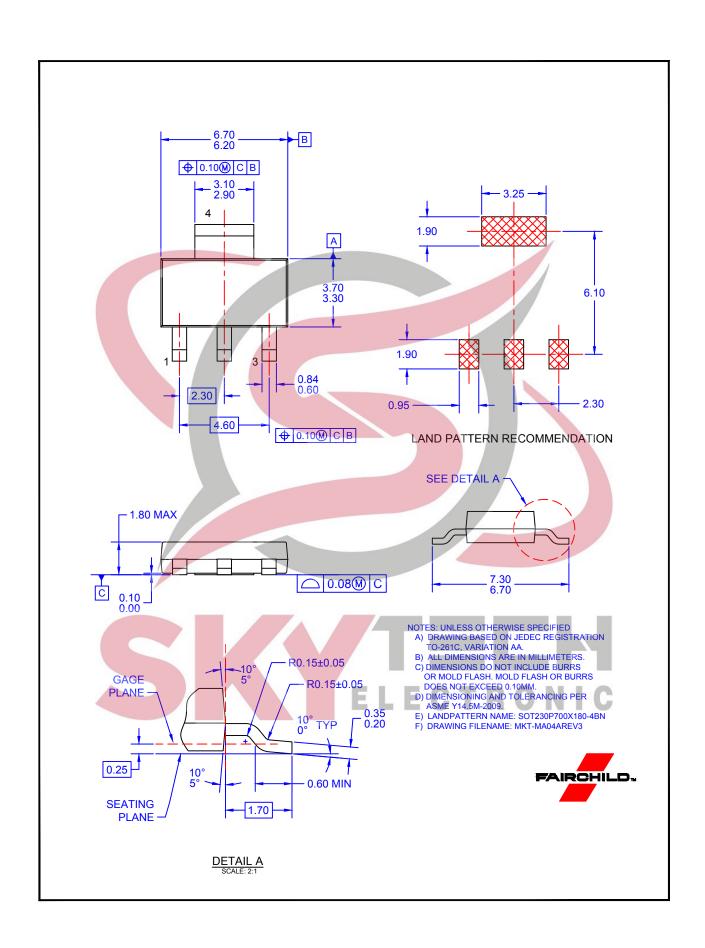
FDT458P Rev. B(W)



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